MAR 0 9 2004
PATENT & TRADEMARK OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Haynie
Serial No.: 10/679,220
Filing Date: 10/03/03

Art Unit: TBD
Examiner: TBD
Docket No.: TI-34351.1

Title: BLADED SILICON-ON-INSULATOR SEMICONDUCTOR DEVICES AND METHOD OF MAKING

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner for Patents
ATTN: Official Draftsperson
Washington, D.C. 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on:
3-4-04
Tammie Chambers

Dear Sir:

In response to the Notice To File Corrected Application Papers dated January 2, 2004 requiring formal drawings of the above identified application, please find enclosed five (5) sheets of formal drawings as required by the Examiner.

Respectfully submitted,

W. Daniel Swayze, Jr.
Reg. No. 34,478
Attorney for Applicants

TEXAS INSTRUMENTS INCORPORATED
P. O. Box 655474, MS 3999
Dallas, Texas 75265-5474
(972) 917-5633